UNITED STATES PATENT AND TRADEMARK OFFICE

# CERTIFICATE OF CORRECTION

PATENT NO. : 7,184,710 B2
APPLICATION NO. : 09/932447
DATED : February 27, 2007
INVENTOR(S) : Hogan



It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the Face Page, in Field (54), in "Title", in Column 1, Line 2, delete "LUR" and insert -- IUR --, therefor.

On Page 2, in Field (56), under "OTHER PUBLICATIONS", in Column 2, Line 28, delete "08/852,915" and insert -- 09/852,915 --, therefor.

Delete Drawing Sheets 6 & 7, and Replace with Drawing Sheets 6 & 7 (Attached).

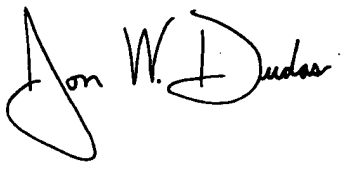
In Column 1, Line 2, delete "LUR" and insert -- IUR --, therefor.

In Column 32, Line 63, in Claim 14, delete "network," and insert -- network --, therefor.

In Column 34, Line 36, in Claim 33, delete "stared" and insert -- stored --, therefor.

Signed and Sealed this

Eighth Day of April, 2008



JON W. DUDAS
*Director of the United States Patent and Trademark Office*

120A(1)

| CELL TOPOLOGY TABLE | |
|---|---|
| CELL | NEIGHBORING CELL(S) | FILTERING RULE FOR CELL: ALLOWED SGs |
| $C_{2-1}$ | $C_{1-2}$, $C_{3-1}$, $C_{3-2}$, $C_{2-2}$ | ALLOW: SG1, SG2 |
| $C_{2-2}$ | $C_{2-1}$, $C_{3-2}$ | ALLOW: SG1, SG2 |
| $C_{1-2}$ | | ALLOW: SG1, SG2 |
| $C_{3-1}$ | | ALLOW: SG1 |
| $C_{3-2}$ | | ALLOW: SG1 |

Fig. 5A(1)

120B(1)

| CELL TOPOLOGY TABLE | |
|---|---|
| CELL | NEIGHBORING CELL(S) | FILTERING RULE FOR CELL: DISALLOWED SGs |
| $C_{2-1}$ | $C_{1-2}$, $C_{3-1}$, $C_{3-2}$, $C_{2-2}$ | DISALLOW: ... |
| $C_{2-2}$ | $C_{2-1}$, $C_{3-2}$ | DISALLOW: ... |
| $C_{1-2}$ | | DISALLOW: ... |
| $C_{3-1}$ | | DISALLOW: SG2 |
| $C_{3-2}$ | | DISALLOW: SG2 |

Fig. 5B(1)

120C(1)

| CELL TOPOLOGY TABLE | | |
|---|---|---|
| CELL | NEIGHBORING CELL(S) | FILTERING RULE FOR CELL: ALLOWED SBs | |
| $C_{2-1}$ | $C_{1-2}$, $C_{3-1}$, $C_{3-2}$, $C_{2-2}$ | BITMAP: | SG1 | SG2 |
| $C_{2-2}$ | $C_{2-1}$, $C_{3-2}$ | BITMAP: | SG1 | SG2 |
| $C_{1-2}$ | | BITMAP: | SG1 | SG2 |
| $C_{3-1}$ | | BITMAP: | SG1 | SG2 |
| $C_{3-2}$ | | BITMAP: | SG1 | SG2 |

Fig. 5C(1)

120D(1)

| CELL TOPOLOGY TABLE | | | | |
|---|---|---|---|---|
| CELL | NEIGHBORING CELL(S) | FILTERING RULE FOR CELL: DISALLOWED SGs | | |
| $C_{2-1}$ | $C_{1-2}$, $C_{3-1}$, $C_{3-2}$, $C_{2-2}$ | BITMAP: | SG1 | SG2 |
| $C_{2-2}$ | $C_{2-1}$, $C_{3-2}$ | BITMAP: | SG1 | SG2 |
| $C_{1-2}$ | | BITMAP: | SG1 | SG2 |
| $C_{3-1}$ | | BITMAP: | SG1 | SG2 |
| $C_{3-2}$ | | BITMAP: | SG1 | SG2 |

Fig. 5D(1)

120A(2)

| CELL TOPOLOGY TABLE | |
|---|---|
| CELL | NEIGHBORING CELL(S) | FILTERING RULE FOR CELL: ALLOWED IMSI OR PLMNid |
| $C_{2-1}$ | $C_{1-2}$, $C_{3-1}$, $C_{3-2}$, $C_{2-2}$ | IMSI->OP1, OP2 |
| $C_{2-2}$ | $C_{2-1}$, $C_{3-2}$ | IMSI->OP1, OP2 |
| $C_{1-2}$ | | IMSI->OP1, OP2 |
| $C_{3-1}$ | | IMSI->OP1 |
| $C_{3-2}$ | | IMSI->OP1 |

Fig. 5A(2)

120B(2)

| CELL TOPOLOGY TABLE | |
|---|---|
| CELL | NEIGHBORING CELL(S) | FILTERING RULE FOR CELL: DISALLOWED IMSI OR PLMNid |
| $C_{2-1}$ | $C_{1-2}$, $C_{3-1}$, $C_{3-2}$, $C_{2-2}$ | IMSI->... |
| $C_{2-2}$ | $C_{2-1}$, $C_{3-2}$ | IMSI->... |
| $C_{1-2}$ | | IMSI->... |
| $C_{3-1}$ | | IMSI->OP2 |
| $C_{3-2}$ | | IMSI->OP2 |

Fig. 5B(2)

120C(2)

| CELL TOPOLOGY TABLE | | |
|---|---|---|
| CELL | NEIGHBORING CELL(S) | FILTERING RULE FOR CELL: ALLOWED IMSI OR PLMNid |
| $C_{2-1}$ | $C_{1-2}$, $C_{3-1}$, $C_{3-2}$, $C_{2-2}$ | BITMAP: OP1 OP2 |
| $C_{2-2}$ | $C_{2-1}$, $C_{3-2}$ | BITMAP: OP1 OP2 |
| $C_{1-2}$ | | BITMAP: OP1 OP2 |
| $C_{3-1}$ | | BITMAP: OP1 OP2 |
| $C_{3-2}$ | | BITMAP: OP1 OP2 |

Fig. 5C(2)

120D(2)

| CELL TOPOLOGY TABLE | | |
|---|---|---|
| CELL | NEIGHBORING CELL(S) | FILTERING RULE FOR CELL: DISALLOWED IMSI OR PLMNid |
| $C_{2-1}$ | $C_{1-2}$, $C_{3-1}$, $C_{3-2}$, $C_{2-2}$ | BITMAP: OP1 OP2 |
| $C_{2-2}$ | $C_{2-1}$, $C_{3-2}$ | BITMAP: OP1 OP2 |
| $C_{1-2}$ | | BITMAP: OP1 OP2 |
| $C_{3-1}$ | | BITMAP: OP1 OP2 |
| $C_{3-2}$ | | BITMAP: OP1 OP2 |

Fig. 5D(2)